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501.39619X00/219900825US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

SHIMODA, et al.

Serial No.:

09/783,604

Filed:

February 15, 2001

For:

A METHOD AND SYSTEM FOR ANALYZING CIRCUIT  
PATTERN DEFECTS

Group:

2623

Examiner:

S. Ahmed

Conf. No.:

9403

REQUEST FOR PRIORITY ACKNOWLEDGEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

November 2, 2005

Sir:

A letter claiming right of priority and filing the priority document was forwarded to the Patent Office on February 15, 2001 as evidenced by the return postcard (copy attached).

In view of the foregoing information it is respectfully requested that a formal acknowledgement of receipt of the claim of priority and the priority document in this application be made.

Respectfully submitted,

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Patent

Trademark

501.39619X00

Serial No. \_\_\_\_\_ Filed Feb. 15, 2001

Applicant(s) SHIMODA, et al

Papers filed herewith on Feb. 15, 2001

Fees \$1,054.00  Assignment w/ltr

New Application w/decl.  Letter to Draftsman

Amendment

Priority Documents w/ltr

Notice of Appeal

Petition for Ext. of Time

Appeal Brief

9 Sheets of Formal Drawings

Other Specification, (28) claims, Abstract (36pgs)  
Decl. (5pgs), Figs. 1-15, Credit Card Payment

Form

Receipt is hereby acknowledged of the papers filed as indicated in connection with above identified case.

(DC)

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COMMISSIONER OF PATENTS AND TRADEMARKS

219900825US1

